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hereby certify that this correspondence is being hand filed with the United States Patent and Trademark Office in Alexandria,

on November 24, 2003.

Stephanie R. Mason

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the application of:

Yasuaki HIRANO

Serial No.:

10/052,519

Filing Date:

January 23, 2002

For:

NONVOLATILE SEMICONDUCTOR MEMORY DEVICE CAPABLE OF

PREVENTING OCCURRENCE OF

LATCH-UP (As amended)

Examiner: Hoai V. Ho

Group Art Unit: 2818

AMENDMENT UNDER 37 CFR 1.111

Mail Stop Non-Fee Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In response to the Office Action mailed June 26, 2003, please amend this application as

follows:

The Amendments to the Specification begin on page 2 of this paper.

The Amendments to the Claims begin on page 3 of this paper.

The Remarks begin on page 5 of this paper.

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DEC -8 2003

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